

# Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

56645

Application ID:

10613246

**Integrated Circuit Memory** 

**Devices Having Clock Signal** 

Title of Invention:

Arbitration Circuits Therein and

Methods of Performing Clock

Signal Arbitration

First Named Inventor:

Mario Au

Domestic/Foreign Application:

**Domestic Application** 

Filing Date:

2003-07-03

Effective Receipt Date:

2004-03-05

Submission Type:

Information Disclosure

Statement

Filing Type:

Confirmation number:

4741

Attorney Docket Number:

5646-42XX

Total Fees Authorized:

Digital Certificate Holder: cn=Grant J. Scott,ou=Registered Attorneys,ou=Patent and Trademark

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#### **TRANSMITTAL**

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention Integrated Circuit Memory Devices Having Clock Signal Arbitration Circuits Therein and Methods of Performing Clock Signal Arbitration

Application Number:

10/613246

Date:

2003-07-

First Named Applicant:

Mario Au

Confirmation Number:

4741

Communation (Value of )

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Attorney Docket Number: 5646-42XX

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Submitted by:	Elec. Sign.	Sign. Capacity	
Grant J. Scott Registered Number: 36925	/gjs/	Attorney	

Documents being submitted

**Files** 

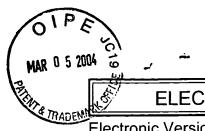
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5646-42XX-usidst.xml

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Comments



## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Integrated Circuit Memory Devices Having Clock Signal Arbitration Circuits Therein and Methods of Performing Clock Signal Arbitration

Application Number:

10/613246

Confirmation Number:

4741

First Named Applicant:

Mario Au

Attorney Docket Number: 5646-42XX Art Unit:

2818

Search string:

( 6557053 or 6366529 or 6259648 or 5442747

or 4888741 ).pn.

#### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6557053	2003-04-29	Bass et al.	B1	710	29
	2	6366529	2002-04-02	Williams et al.	B1	365	239
	3	6259648	2001-07-10	Kragick	B1	365	230.05
	4	5442747	1995-08-15	Chan et al.		395	164
	5	4888741	1989-12-19	Malinowski		365	230.05

## Signature

Examiner Name	Date		
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